

Vak: Digitale Techniek 2

credits: 2

Vakcode ELVP19GDIG2
Naam Digitale Techniek 2
Studiejaar 2020-2021
ECTS credits 2
Taal Nederlands
Coördinator J.J. Dallinga

Werkvormen Hoorcollege
Practicum / Training
Toetsen Digitale techniek 2 - Schriftelijk, organisatie
tentamenbureau
Digitale techniek 2 Labs - Vaardigheidstoets

Leeruitkomsten

The student is able to:

- design and build shift registers and counters using standard memory elements;
- design and build state machines with hardware;
- explain the principle of a state machine.

Inhoud

During this unit the student will learn about Boolean algebra, combinatory logic and state machines. This will enable the student to understand the architecture and internal operation of a CPU and its peripherals.

During the theory lessons a few practical's are performed. (Practical is not graded).

Opgenomen in opleiding(en)

Elektrotechniek Major Elektronica
Elektrotechniek Major Mechatronica

School(s)

Instituut voor Engineering

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